

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
COMPUTER NETWORKS (23CS502)
UNIT-II

S.No	PART-A(Short Questions)		BT	CO	PO
1	Data link protocols almost always put the CRC in a trailer, rather than in A header .Why?		L4	CO2	PO2
2	Compare and contrast flow control and error control.		L3	CO2	PO2
3	Compare and contrast error detection codes and error correction codes		L3	CO2	PO2
4	What is slotted ALOHA? Mention its advantages.		L1	CO2	PO1
5	What is CRC Checker?		L1	CO2	PO1
6	What is the functions of LLC?		L1	CO2	PO1
7	What is ARQ and explain its importance		L1	CO2	PO1
8	Explain the importance of sequence number in Stop and Wait ARQ		L1	CO2	PO2
9	Explain is checksum?		L1	CO2	PO1
10	Explain virtual circuit networks		L1	CO2	PO1
Part-B(Long Questions)					
11	a)	What is framing? Explain various framing technologies of data link layer.	L1	CO2	PO1, PO2
	b)	Explain about Error detection and Error Correction	L1	CO2	
12	a)	Explain stop and wait protocol for noisy channel	L1	CO2	PO1, PO2, PO3
	b)	Compare and contrast pure ALOHA and slotted ALOHA channel allocation methods.	L2	CO2	
13	a)	Explain CSMA/CD and CSMA/CA in detail	L1	CO2	PO1, PO2, PO3
	b)	Mention the types of errors and explain each type	L1	CO2	
14	a)	Name the protocols used for CSMA	L1	CO2	PO1, PO2, PO3
	b)	Explain Go-Back-N with an neat sketch	L1	CO2	
15	a)	Explain CRC Error detection method with an example	L1	CO2	PO1, PO2, PO3
	b)	Explain Hamming Code in Error Correction	L1	CO2	
16	a)	What are the responsibilities of data link layer	L1	CO2	PO1, PO2
	b)	Briefly discuss about data link layer design issues	L3	CO2	